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0E 1012001	June 7, 2005			05	(Date)			
APPLICATION NO.	FILING DATE	FIRST NAMED INVEN				ATTORNEY DOCKET NO.		
09/772,027	01/29/2001	Kenji Shigeki				F-6810	CONFIRMATION NO.	
TITLE OF INVENTION: LOGIC INTEGRATED CIRCUIT, AND RECORDING MEDIUM READABLE BY A COMPUTED WHICH STORES.								
CORE ON SAID LOGIC INTEGRATED CIRCUIT								
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4140			\$300		\$1700	07/13/2005		
EXAMINER ART U			00.100.000001100		ASS-SUBCLASS			
TREAT, WILLIAM M 2183								
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Authorized Signature	Date_ June 7, 2005							
Typed or printed name	Registration No. 22,389							
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No. F-6810

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Applicant

Kenji SHIGEKI, et al.

Serial No.

09/772,027

Filed

January 29, 2001

For

LOGIC INTEGRATED CIRCUIT, AND RECORDING

MEDIUM READABLE BY A COMPUTER, WHICH STORES A SOURCE OF CPU CORE ON SAID LOGIC

INTEGRATED CIRCUIT

Group Art Unit

2183

Examiner

William M. Treat

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Allowance Date

April 13, 2005

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Respectfully submitted,

Jordan and Hamburg LLP

C. Bruce Hamburg

Reg. No. 22,389

Attorney for Applicants

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